

Biography:

Hideaki Kimura received the **B.E.** and **M.E.** and **Ph.D.** degrees in electrical engineering from **Hokkaido University, Sapporo, Japan**, in 1987, 1989, and 1992, respectively. In 1992, he joined NTT LSI Laboratories, Kanagawa, Japan, where he engaged in research on package design for 100Gb/s level high-frequency wideband ICs and high-sensitivity receiver module design for optical subscriber systems. From 1994 to 1996, he was at the **NTT Transmission Systems Laboratories**, Kanagawa, Japan, where he engaged in research on economical system such as LD transceiver, PD transceiver techniques. From 1996, he has been engaged in research on super-low-power transceiver module for optical subscriber system, system design and basic technologies such as 1V-operation-super-small-size ONU and no-power-supply voice communication service for black out situation due to disaster. Furthermore, he has been researched future optical access network systems based on WDM and DSP. From 2010 to 2012, he was an executive research engineer at research & development planning section. Now he is a general manager of 1st promotion project, **NTT Access Network Service Systems Laboratories**. He is a member of **IEEE** and **IEICE**.